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Application ID:

10699505

Title of Invention:

CHANNELED FLAT PLATE FIN HEAT EXCHANGE SYSTEM,

DEVICE AND METHOD

First Named Inventor:

Girish Upadhya

Domestic/Foreign Application: Domestic Application

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Information Disclosure

Statement

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Digital Certificate Holder: cn=Thomas B. Haverstock,ou=Registered Attorneys,ou=Patent and

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TRANSMITTAL

Electronic Version v1.1

TRADEN Stylesheet Version v1.1.0

Title of Invention

CHANNELED FLAT PLATE FIN HEAT EXCHANGE SYSTEM, DEVICE AND METHOD

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Submitted by:	Elec. Sign.	Sign. Capacity	
Thomas B. Haverstock Registered Number: 32571	/tbh/	Attorney	

Documents being submitted

Files

us-ids

COOL00601-usidst.xml

us-ids.dtd

us-ids.xsl

Comments



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

Title of Invention

CHANNELED FLAT PLATE FIN HEAT EXCHANGE SYSTEM, DEVICE AND METHOD

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First Named Applicant:

Girish Upadhya

Attorney Docket Number:

Search string:

(3817321 or 5083194 or 5696405 or 5703536 or 5704416 or 5880524 or 5940270 or 5964092 or 6443222 or 6457515 or 6588498).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	3817321	1974-06-18	von Cube et al.			
	2	5083194	1992-01-21	Bartilson			
	3	5696405	1997-12-09	Weld			
	4	5703536	1997-12-30	Davis et al.			
	5	5704416	1998-01-06	Larson et al.			
	6	5880524	1999-03-09	Xie			
	7	5940270	1999-08-17	Puckett			
	8	5964092	1999-10-12	Tozuka et al.			
	9	6443222	2002-09-03	Yun et al.	B1		
	10	6457515	2002-10-01	Vafai et al.	B1		
	11	6588498	2003-07-08	Reysin et al.	B1		

Signature

Examiner Name	Date		